

(3)

H-961



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Patent Application of

T. HIROSE et al

Serial No. 09/774,723

Filed: February 1, 2001

For: POLISHING PAD SURFACE CONDITION EVALUATION METHOD AND AN APPARATUS THEREOF AND A METHOD OF PRODUCING A SEMICONDUCTOR DEVICE

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents
Washington, D.C. 20231

March 28, 2001

Sir:

Applicants also submit herewith twelve (12) sheets of formal drawings (Figs. 1-20), and request that they be substituted for the drawings now in the application. No changes have been made to the drawings.

Please charge any additional fees resulting from this action to Deposit Account No. 50-1417.

Respectfully submitted,

Daniel J. Stanger
Registration No. 32,846
Attorney for Applicants

MATTINGLY, STANGER & MALUR, P.C.
104 East Hume Avenue
Alexandria, Virginia 22301
(703) 684-1120
Date: March 28, 2001

APPROVED	O. G. FIG.
BY	CLASS SUBCLASS
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FIG.1

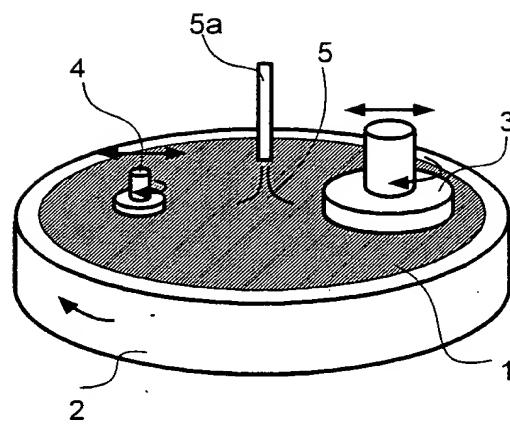


FIG.2

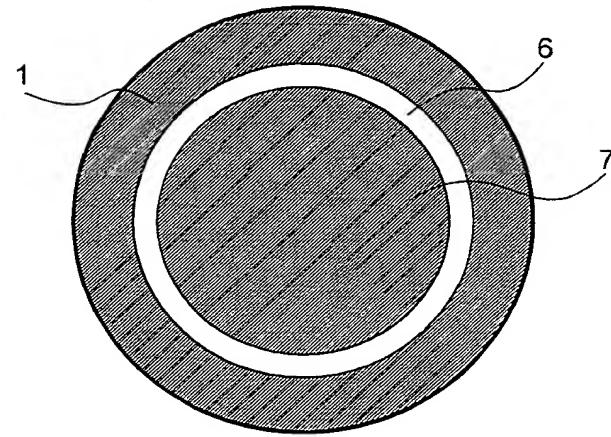


FIG.3

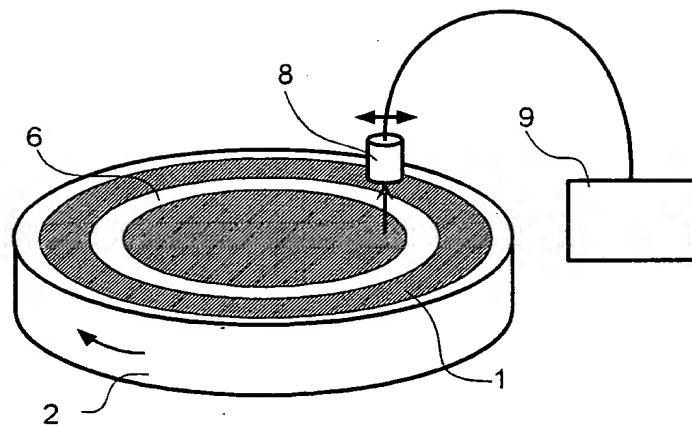
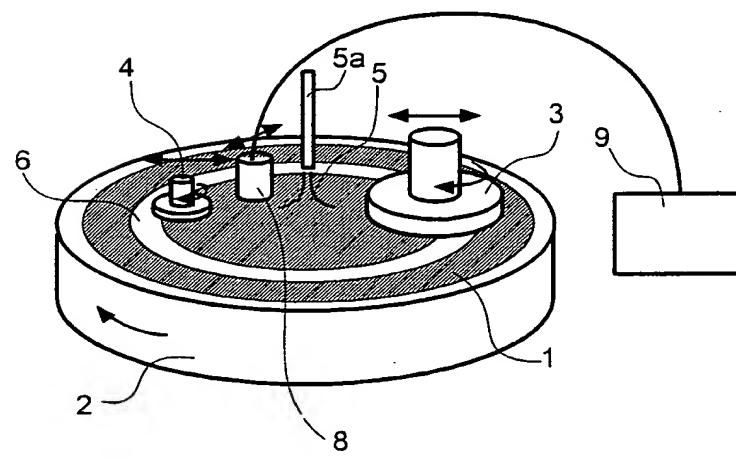


FIG.4



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FIG.5

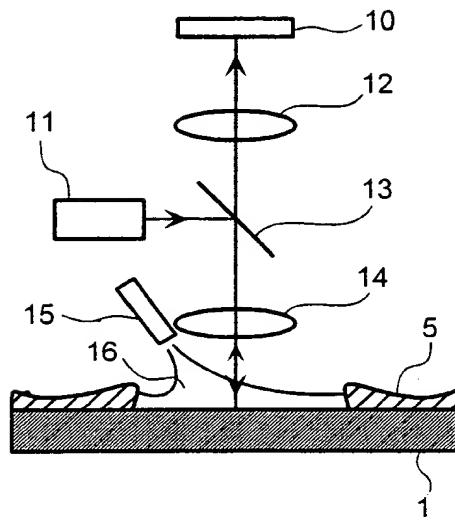
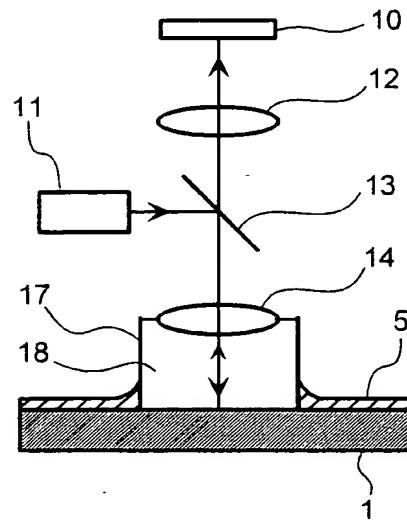


FIG.6

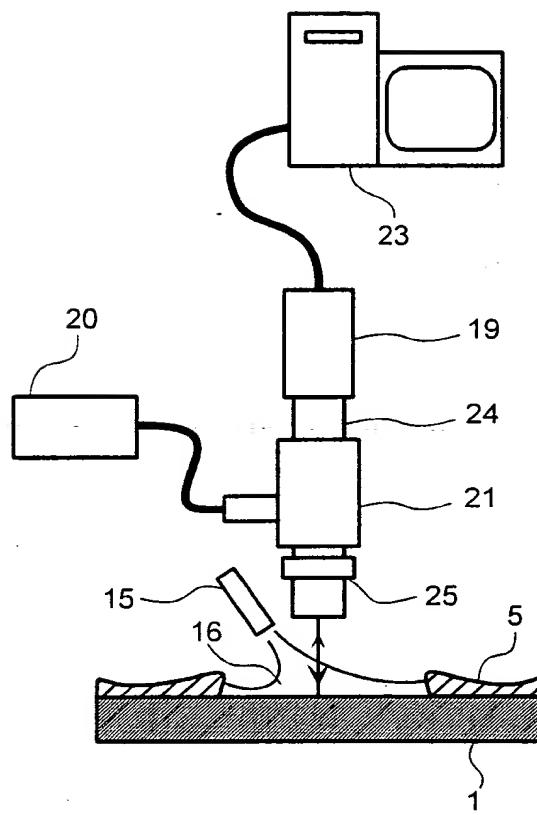


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CLASS SUGOL

FIG.7



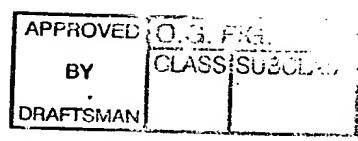


FIG. 8

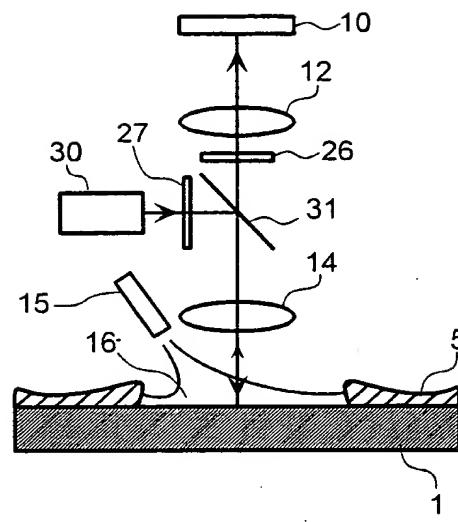


FIG. 9

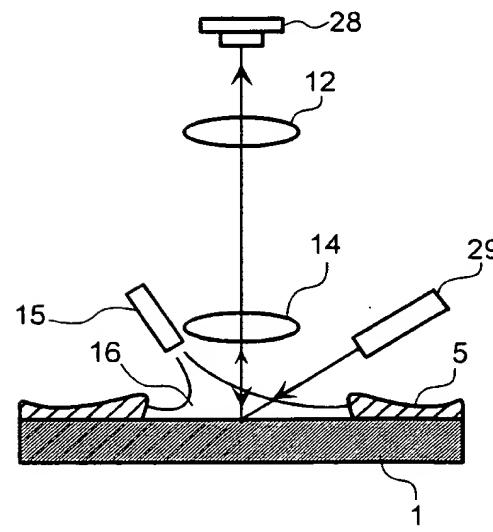
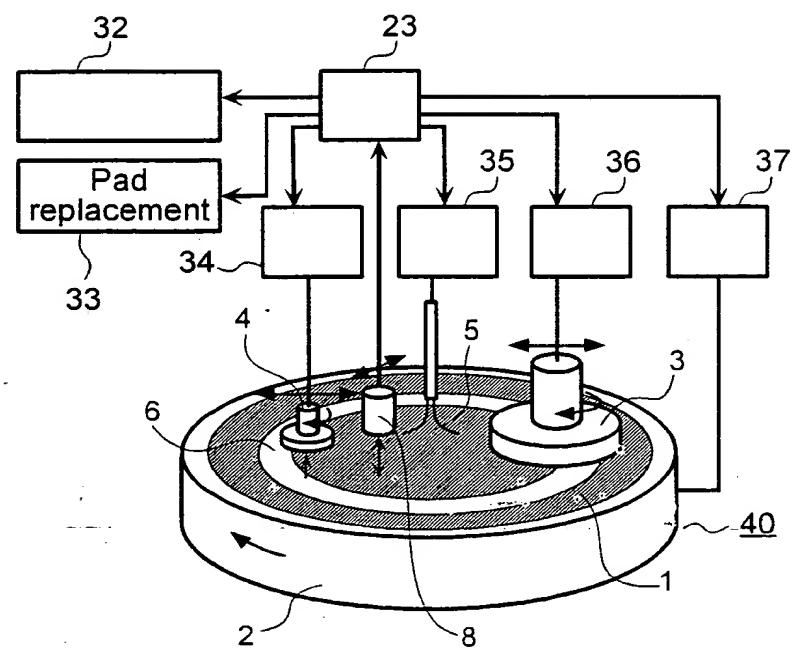


FIG.10



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FIG.11

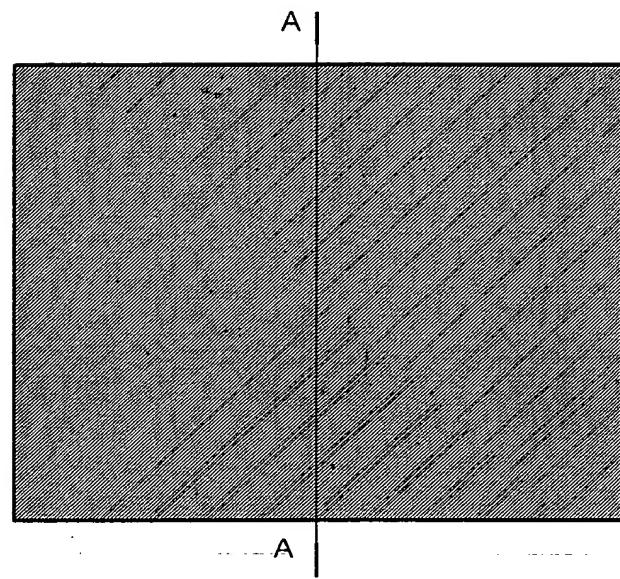
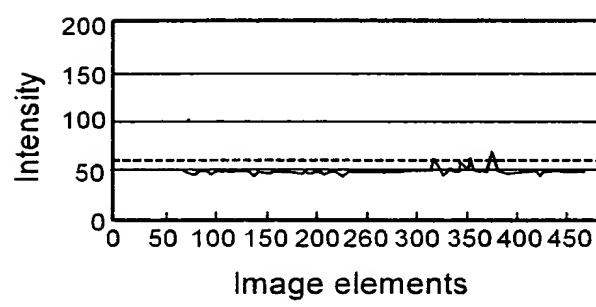


FIG.12



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FIG.13

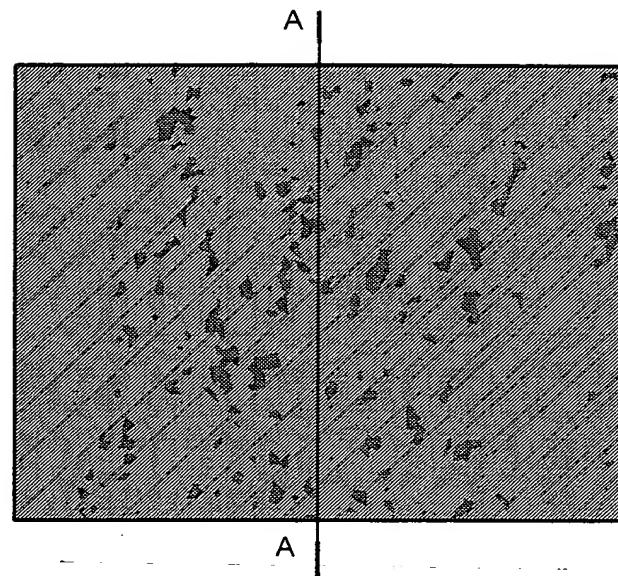
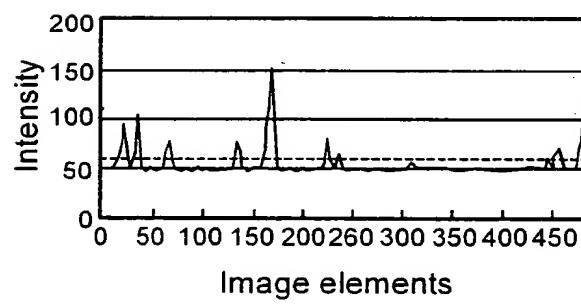
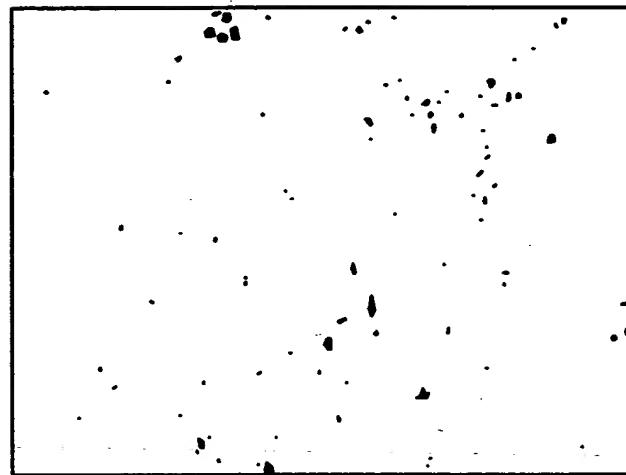


FIG.14



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FIG.15



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FIG.16

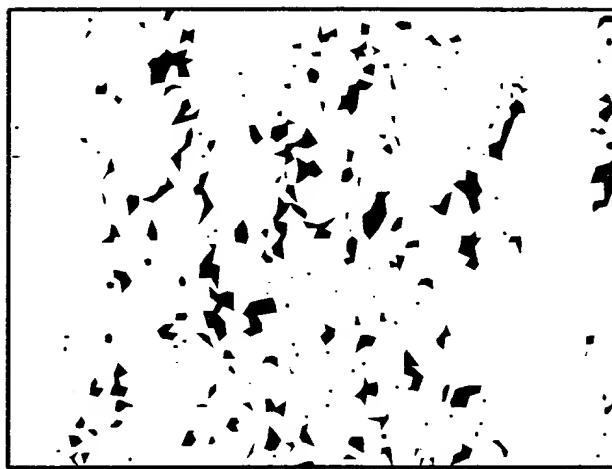
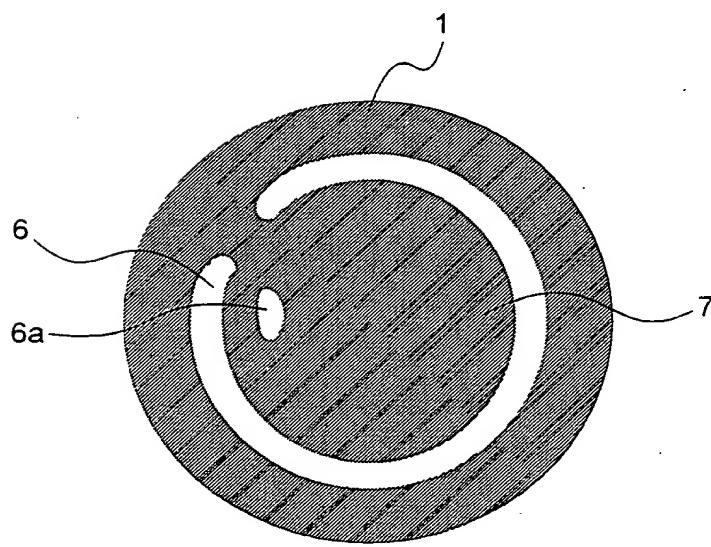


FIG.17



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FIG.18

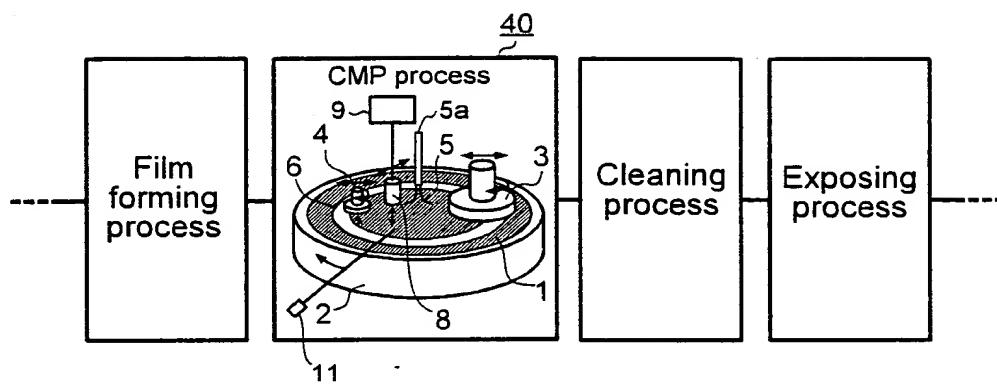
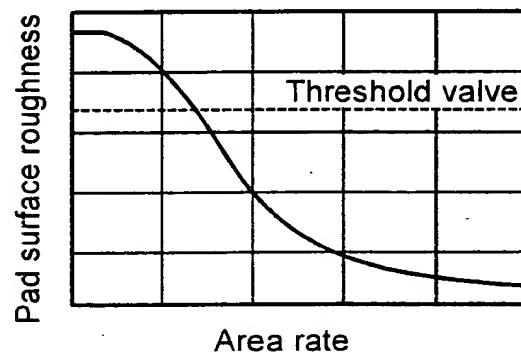


FIG.19



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FIG.20

